

Core-edge Integrated Scenario with a High-Performance Hybrid Core, Naturally Small ELMs, and a Dissipative Divertor on DIII-D

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Speaker's email address

victorb@fusion.gat.com

Speaker's Affiliation

Lawrence Livermore National Laboratory, Livermore

Member State or IGO

United States

Gender Survey (Speaker Only)

Mr

Author: VICTOR, Brian (Lawrence Livermore National Laboratory)

Co-authors: HYATT, Alan (General Atomics); BORTOLON, Alessandro (PPPL); Dr LASNIER, Charlie (Lawrence Livermore National Laboratory); HOLCOMB, Christopher (Lawrence Livermore National Laboratory); PETTY, Craig (General Atomics); KHABANOV, Filipp (University of Wisconsin Madison); WANG, Huiqian (General Atomics); CHEN, Jie (University of California Los Angeles); SHAFER, Morgan (Oak Ridge National Laboratory); LI, Nami (Lawrence Livermore National Laboratory); HU, Qiming (PPPL); PERILLO, Renato (University of California, San Diego); WILCOX, Robert (Oak Ridge National Laboratory); DING, Siye (General Atomics); Dr OSBORNE, Thomas (General Atomics); CHEN, Xi (General Atomics); LI, Zeyu (General Atomics); XU, xueqiao (Lawrence Livermore National Laboratory)

Presenter: VICTOR, Brian (Lawrence Livermore National Laboratory)

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