

Session Program

4-7 Nov 2019

Third IAEA Technical Meeting on Divertor Concepts

Divertors in Next Step Devices

IAEA Headquarters, Vienna, Austria, Board Room C (C Building - 4th Floor)

Thursday 7 November

09:00

Divertors in Next Step Devices

Session | Location: IAEA Headquarters, Vienna, Austria, Board Room C (C Building, 4th Floor)

09:00–09:30 **The first ITER tungsten divertor: what do we hope to learn?**

Speaker

Richard Pitts

09:30–09:50 **Recent progress on divertor physics design of CFETR**

Speaker

Rui Ding

09:50–10:10 **A strategy to develop power exhaust solutions for tokamaks beyond ITER**

Speaker

John Canik

10:10

10:30

Divertors in Next Step Devices: Continued

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10:30–10:50 **Power exhaust studies in the Divertor Tokamak Test facility**

Speaker

Nicola Vianello

10:50–11:10 **The physical design of EAST lower tungsten divertor by SOLPS modeling**

Speaker

Prof. Chaofeng Sang

11:10